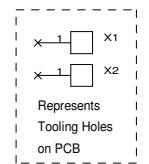
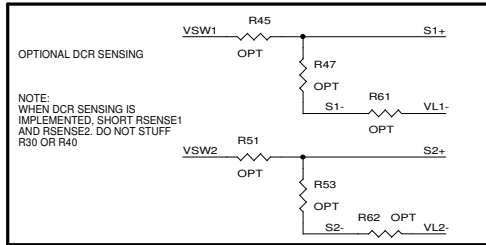
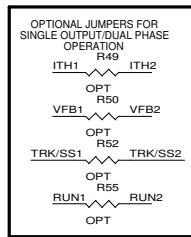
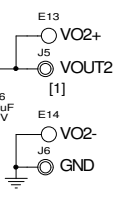
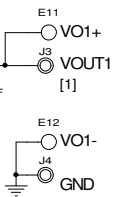
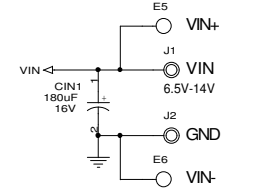
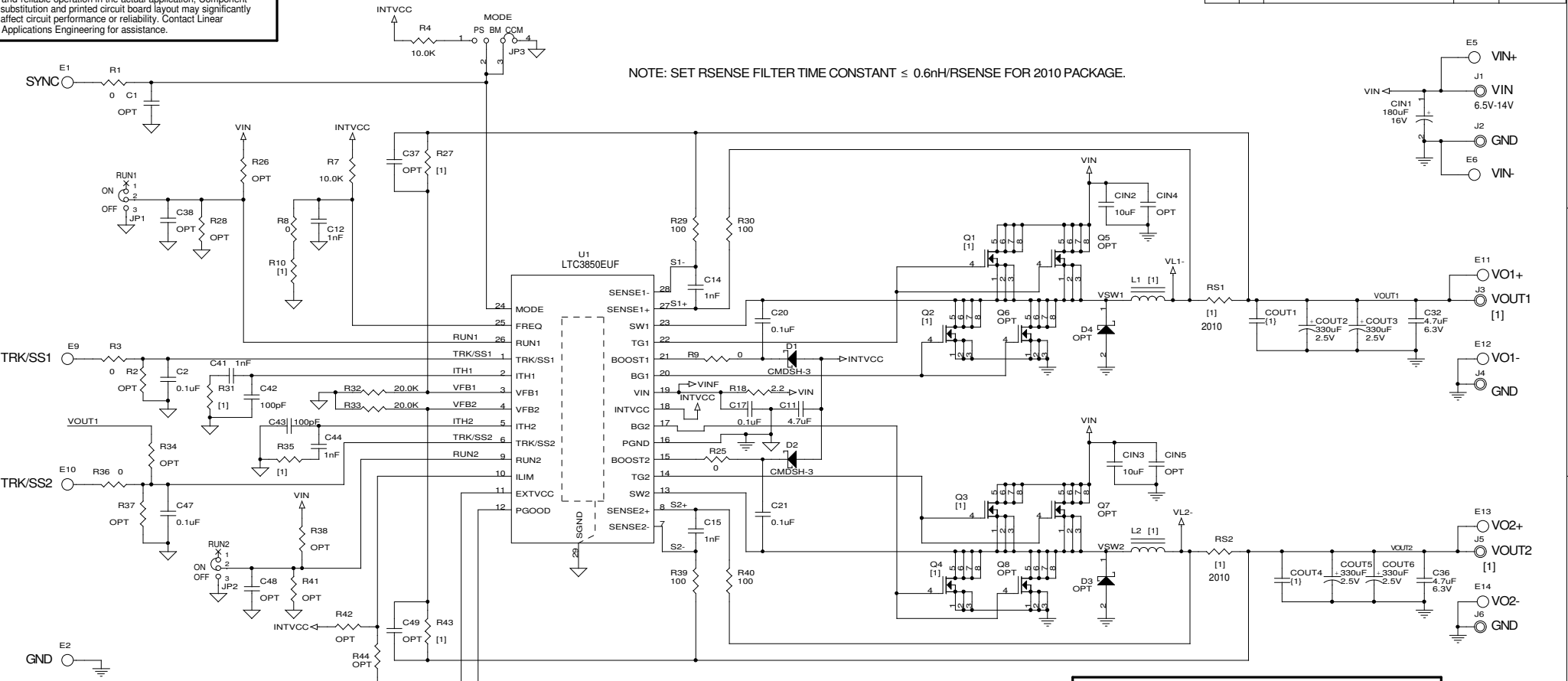


This circuit is proprietary to Linear Technology and supplied for use with Linear Technology parts.  
**Customer Notice:** Linear Technology has made a best effort to design a circuit that meets customer-supplied specifications; however, it remains the customer's responsibility to verify proper and reliable operation in the actual application. Component substitution and printed circuit board layout may significantly affect circuit performance or reliability. Contact Linear Applications Engineering for assistance.

REVISION HISTORY				
ECO	REV	DESCRIPTION	DATE	APPROVED
	2	PROTO	03/21/07	

NOTE: SET RSENSE FILTER TIME CONSTANT  $\leq 0.6nH/RSENSE$  FOR 2010 PACKAGE.



NOTE:  
 FSW = 500Hz For Version A  
 FSW = 400Hz For Version b

[1] VERSION	TOP FEED BACK R		R1, RS2	L1, 2	Q1, 3	Q2, 4	R27	R43	R31, R35	R10	COUT1/COUT4
	VOUT1	VOUT2									
- A	2.0V/10A	1.8V/10A	0.003	TOKO FDU0650-R56M=P3	VISHAY Si4420BDY	VISHAY Si4630DY	30.1K	25.5K	8.66K	3.16K	47uF
- B	1.5V/15A	1.2V/15A	0.002	VITEC 59PR9875	RENEASAS RJK0305DPB	RENEASAS RJK0301DPB	17.8K	10.0K	5.9K	2.55K	100uF

CONTRACT NO.	
APPROVALS	DATE
DRAWN MEI	10/31/06
CHECKED	
APPROVED	
ENGINEER	
DESIGNER	
Thursday, July 19, 2007	

**LINEAR TECHNOLOGY**  
 1630 McCarthy Blvd.  
 Milpitas, CA 95035  
 Phone: (408)332-1800  
 Fax: (408)334-0507

TITLE: SCH, LTC3850EUF, DUAL PHASE/DUAL OUTPUT SYNCHRONOUS BUCK CONVERTER

SIZE: CAGE CODE: DWG NO: DC1185A REV 2

SCALE: NONE FILENAME: 1185A-2.DSN SHEET 1 OF 1